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**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Original) A method for designing an alternating phase shifting mask (altPSM) for projecting an image on an image plane, the method comprising:

providing a circuit layout;

identifying a critical element of said circuit layout, said critical element having a layout dimension (LW), said layout dimension corresponding to a target image dimension in the image plane;

providing a relationship between phase shape width and said target image dimension;

selecting an optimal phase shape width so that said relationship has an optimal value; and

generating a phase shape disposed adjacent to said layout dimension wherein said phase shape has said optimal phase shape width.

2. (Currently Amended) ~~The method of claim 1~~ A method for designing an alternating phase shifting mask (altPSM) for projecting an image on an image plane, the method comprising:

providing a circuit layout;

identifying a critical element of said circuit layout, said critical element having a layout dimension (LW), said layout dimension corresponding to a target image dimension in the image plane;

providing a relationship between phase shape width and said target image dimension;

selecting an optimal phase shape width so that said relationship has an optimal value; and

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generating a phase shape disposed adjacent to said layout dimension wherein said phase shape has said optimal phase shape width.

wherein said relationship comprises across-chip line width variation (ACLV), and said selecting further comprises minimizing said ACLV.

3. (Original) The method of claim 2 wherein said providing a relationship further comprises providing at least one distribution of process errors in at least one lithographic parameter and computing a set of ACLV realizations for said target image dimension and computing the standard deviation of said ACLV realizations for said target image dimension.
4. (Original) The method of claim 3 wherein said at least one lithographic parameter is selected from the group consisting of dose, focus, lens aberration, mask critical dimension error, transmission error, phase error, mask manufacturability error, and a combination thereof.
5. (Original) The method of claim 4 wherein said at least one distribution comprises a gaussian distribution.
6. (Original) The method of claim 1 wherein said relationship comprises process window, and said selecting further comprises maximizing said process window.
7. (Original) The method of claim 1 wherein said providing a relationship comprises providing a lookup table having optimal phase shape widths for a range of target image dimensions stored therein.
8. (Currently Amended) The method of claim 1 wherein said relationship comprises a requirement that said optimal phase shape width be in the range ~~about~~ 0.8-1.2 x LW.

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9. (Original) A computer program product comprising a computer readable storage medium having stored therein instructions executable by the computer for performing a method for designing an alternating phase shifting mask (altPSM) for projecting an image on an image plane, the method comprising:

providing a circuit layout;

identifying a critical element of said circuit layout, said critical element having a layout dimension (LW), said layout dimension corresponding to a target image dimension in the image plane;

providing a relationship between phase shape width and said target image dimension;

selecting an optimal phase shape width so that said relationship has an optimal value; and

generating a phase shape disposed adjacent to said layout dimension wherein said phase shape has said optimal phase shape width.

10. (Currently Amended) ~~The computer program product of claim 9~~ A computer program product comprising a computer readable storage medium having stored therein instructions executable by the computer for performing a method for designing an alternating phase shifting mask (altPSM) for projecting an image on an image plane, the method comprising:

providing a circuit layout;

identifying a critical element of said circuit layout, said critical element having a layout dimension (LW), said layout dimension corresponding to a target image dimension in the image plane;

providing a relationship between phase shape width and said target image dimension;

selecting an optimal phase shape width so that said relationship has an optimal value; and

generating a phase shape disposed adjacent to said layout dimension wherein said phase shape has said optimal phase shape width.

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wherein said relationship comprises across-chip line width variation (ACLV), and said selecting further comprises minimizing said ACLV.

11. (Original) The computer program product of claim 10 wherein said providing a relationship further comprises providing at least one distribution of process errors in at least one lithographic parameter and computing a set of ACLV realizations for said target image dimension and computing the standard deviation of said ACLV realizations for said target image dimension.

12. (Original) The computer program product of claim 11 wherein said at least one lithographic parameter is selected from the group consisting of dose, focus, lens aberration, mask critical dimension error, transmission error, phase error, mask manufacturability error, and a combination thereof.

13. (Original) The computer program product of claim 12 wherein said at least one distribution comprises a gaussian distribution.

14. (Original) The computer program product of claim 9 wherein said relationship comprises process window, and said selecting further comprises maximizing said process window.

15. (Original) The computer program product of claim 9 wherein said providing a relationship comprises providing a lookup table having optimal phase shape widths for a range of target image dimensions stored therein.

16. (Currently Amended) The computer program product of claim 9 wherein said relationship comprises a requirement that said optimal phase shape width be in the range ~~about~~ 0.8-1.2 x LW.

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17. (Canceled)

18. (Canceled)

19. (Canceled)

20. (Canceled)